## **ABSTRACT**

When a storage circuit (13) of a certain stage and those of the following stages are caused to stop, a storage element circuit (11) of a memory area B (2) that stores survivor paths of a particular state is caused to serve as a repeater, and the other storage element circuits (11), which belong to a memory area C (3) are caused to stop, whereby a decoding result can be outputted without using additional bus wires and selectors.

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